



## **Recent Experimental Developments In Nanomagnetic Logic**

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We present recent developments in the field of nano-magnetic logic (NML, formerly a.k.a. M-QCA) where planar, magnetically-coupled, nanometer-scale magnets are assembled into networks that perform binary computation driven by global ("clocking ") fields and local ("input") fields.

We demonstrate the operation of basic elements of NML architecture (binary wire, three input majority logic gate, fanout and their combinations); our work on magnet shape engineering; and discuss interfacing of such systems with conventional CMOS-based logic.